

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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PATENT NO. : 5,648,642
DATED : July 15, 1997
INVENTOR(S) : Robert J. Miller and Stephen J. Bisset

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 13, line 37: delete "N-channel MOS current-limiting".

Column 15, line 31: replace "a" with --an--.

Column 18, lines 6 to 7: replace "circuit Z sum 62" with --Z sum circuit 62--.

Column 20, line 41: replace "ND" with --A/D--.

Column 25, line 64: replace "each, individual" with --each individual--.

Signed and Sealed this

Third Day of February, 1998

Attest:



BRUCE LEHMAN

Attesting Officer

Commissioner of Patents and Trademarks